

The recent database difficulties have been resolved. Please let us know if you encounter any data corruptions.



Find:

Searching for hash and table or list or directory and request or access and microprocessor or microcontroller or processor and multiprocessor.

Restrict to: Header Title Order by: Citations Hubs Usage Date Try: Google (CiteSeer) Google (Web) Yahoo! MSN CSB DBLP

More than 10000 results. **Only retrieving 125 documents (System busy - maximum reduced).** Retrieving documents... **Order: citations weighted by year.**

Mining Sequential Patterns: Generalizations And Performance.. - Srikant, Agrawal (1996) (Correct) (150 citations)
two techniques to solve this problem: 1. We use a **hash-tree** data structure to reduce the number of
a **list** of sequences (a leaf node) or a **hash table** (an interior node) In an interior node, each
for a database of sequences, where each sequence is a **list** of transactions ordered by transaction-time, and
www.almaden.ibm.com/cs/people/ragrawal/papers/edbt96_rj.ps

One or more of the query terms is very common - only partial results have been returned. Try Google (CiteSeer).

Effective Compiler Support for Predicated Execution .. - Mahlke, Lin, Chen, .. (1992) (Correct) (135 citations)
paths with subroutine calls or unresolvable memory **accesses** can restrict optimization and scheduling within
for a wide range of superscalar and VLIW **processors**. 1 Introduction Superscalar and VLIW
Cydra 5 System The Cydra 5 system is a VLIW, **multiprocessor** system utilizing a directed-dataflow
cardit.et.tudelft.nl/~steven/ilp/mahlke92.ps.gz

A Historical Application Profiler for Use by Parallel Schedulers - Gibbons (1997) (Correct) (53 citations)
. 94 Bibliography 97 v **List of Tables** 2.1 Application Characteristics .
Examining .94 Bibliography 97 v **List of Tables** 2.1 Application Characteristics .
and the **processor** time by the number of **processors requested**, and observes that most jobs **requested** a
ftp.cs.toronto.edu/pub/reports/csrg/354/TR-354.ps.Z

Computational Complexity of Planning and Approximate.. - Baral, Kreinovich, Trejo (1999) (Correct) (20 citations)
These results can be represented by the following **table**: exact 0-approximation planning complete
reminder In the language A, we start with a finite **list** of properties (fluents) f 1 fn which
cs.utep.edu/chitta/papers/complexity-ijcai.ps

Probabilistic Frame-Based Systems - Koller, Pfeffer (1998) (Correct) (28 citations)
is specified using a conditional probability **table** (CPT) as in Bayesian networks. For each
type of a simple slot is an explicitly enumerated **list** of possible values for the slot. For example, the
l2r.cs.uiuc.edu/~danr/Teaching/CS491-98/Papers/koller.ps

A Static Parameter based Performance Prediction Tool for.. - Fahringer, Zima (1993) (Correct) (68 citations)
CML =43433:6. The measured runtime for L yields **Table** 1.4 different program versions V jP A j jP B j
prediction. We also do not claim that the **list** of parameters as indicated is complete. in detail
the mask is constant true. **Processors** can only **access** local data. Non-local data referenced by a
www.par.univie.ac.at/~tf/papers/p3t/japan.ps

The Tenet Real-Time Protocol Suite: Design.. - Banerjee.. (1996) (Correct) (43 citations)
for error recovery. The RCAP messages are shown in **Table** 1. Channel establishment is performed in a single
measurements of the protocols. Section 6 provides a **list** of the lessons that we learned during this
Description establish_request Downstream **Request** to establish a new channel. establish_accept
www.cc.gatech.edu/computing/Telecomm/srg/papers2/Tenetoverview.ps.gz

Support Vector Machine Reference Manual - Saunders, Stitson, Weston.. (1998) (Correct) (21 citations)
on the training set. For the multiclass machine a **table** is displayed giving the number of errors on the
to enter parameters, then you are faced with a **list** of the current parameter settings, and a menu. An
to do is to place the MINOS Fortran code into the **directory** minos.f, change the MINOS setting in
www.clrc.rhbc.ac.uk/research/SVM/pub/report98-03.ps

How to Convert Any Digital Signature Scheme Into a Group.. - Petersen (1997) (Correct) (28 citations)
signature scheme. As tools we use a cryptographic **hash** function h, a probabilistic public key
of [GoMR88]1.1 Related work The following **table** compares the properties of some of the proposals
many of the group members (or more generally which **access** structure) signed a message. This might be
www.geocities.com/CapeCanaveral/Lab/8967/PBGroupSignatures.ps.gz

Search Strategy

No.	Database	Search term	Info added since	Results
1	INZZ	hash ADJ table	unrestricted	549
2	INZZ	intent ADJ seize	unrestricted	0
3	INZZ	seize ADJ request	unrestricted	0
4	INZZ	address AND (processor OR microprocessor OR microcontroller)	unrestricted	4219
5	INZZ	4 AND 1	unrestricted	2
6	INZZ	hash ADJ (table OR 'LIST' OR directory)	unrestricted	550
7	INZZ	6 AND 4	unrestricted	2

Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "((((hash table)<in>metadata))<and>((processor or microprocessor or microcontroller)<i..."))

e-mail printer friendly

Your search matched 10 of 254 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)

[New Search](#)

Modify Search

((((hash table)<in>metadata))<and>((processor or microprocessor or microcontroller)<i..."))

☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

[view selected items](#)

[Select All](#) [Deselect All](#)

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ **1. Hybrid open hash tables for network processors**
Qing Ye; Parson, D.; Liang Cheng;
[High Performance Switching and Routing, 2005. HPSR. 2005 Workshop on 12-14 May 2005 Page\(s\):113 - 117](#)
Digital Object Identifier 10.1109/HPSR.2005.1503205
[AbstractPlus](#) | Full Text: [PDF\(361 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **2. An efficient hash table based approach to avoid state space explosion in history driven quasi-static scheduling**
Lomena, A.G.; Lopez-Vallejo, M.; Watanabe, Y.; Kondratyev, A.;
[Design, Automation and Test in Europe Conference and Exhibition, 2003 2003 Page\(s\):428 - 433](#)
Digital Object Identifier 10.1109/DATE.2003.1253647
[AbstractPlus](#) | Full Text: [PDF\(284 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **3. A Technique to Exploit Memory Locality for Fast Instruction Set Simulation**
Wei Qin; Bo Hu;
[ASIC, 2005. ASICON 2005. 6th International Conference On Volume 2, 24-27 Oct. 2005 Page\(s\):846 - 849](#)
[AbstractPlus](#) | Full Text: [PDF\(1304 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **4. Catching accurate profiles in hardware**
Narayanasamy, S.; Sherwood, T.; Sair, S.; Calder, B.; Varghese, G.;
[High-Performance Computer Architecture, 2003. HPCA-9 2003. Proceedings. The Ninth International Symposium on 8-12 Feb. 2003 Page\(s\):269 - 280](#)
Digital Object Identifier 10.1109/HPCA.2003.1183545
[AbstractPlus](#) | Full Text: [PDF\(425 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **5. ScalParC: a new scalable and efficient parallel classification algorithm for mining large datasets**
Joshi, M.V.; Karypis, G.; Kumar, V.;
[Parallel Processing Symposium, 1998. 1998 IPSP/SPDP. Proceedings of the First Merged International...and Symposium on Parallel and Distributed Processing 1998 30 March-3 April 1998 Page\(s\):573 - 579](#)
Digital Object Identifier 10.1109/IPSP.1998.669983
[AbstractPlus](#) | Full Text: [PDF\(676 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **6. A vectorization technique of hashing and its application to several sorting algorithms**

Kanada, Y.;
[Databases, Parallel Architectures and Their Applications., PARBASE-90, International Conference on](#)
7-9 March 1990 Page(s):147 - 151
Digital Object Identifier 10.1109/PARBSE.1990.77135
[AbstractPlus](#) | Full Text: [PDF\(456 KB\)](#) IEEE CNF
[Rights and Permissions](#)

☐ **7. Hash table and sorted array: a case study of multi-entry data structures in massively parallel systems**

Yen, I.-L.; Leu, D.-R.; Bastani, F.;
[Frontiers of Massively Parallel Computation, 1990. Proceedings., 3rd Symposium on the](#)
8-10 Oct. 1990 Page(s):51 - 54
Digital Object Identifier 10.1109/FMPC.1990.89437
[AbstractPlus](#) | Full Text: [PDF\(332 KB\)](#) IEEE CNF
[Rights and Permissions](#)

☐ **8. Hash table in massively parallel systems**

Yen, I.-L.; Bastani, F.;
[Parallel Processing Symposium, 1992. Proceedings., Sixth International](#)
23-26 March 1992 Page(s):660 - 664
Digital Object Identifier 10.1109/IPPS.1992.222988
[AbstractPlus](#) | Full Text: [PDF\(420 KB\)](#) IEEE CNF
[Rights and Permissions](#)

☐ **9. PRISC software acceleration techniques**

Razdan, R.; Brace, K.; Smith, M.D.;
[Computer Design: VLSI in Computers and Processors, 1994. ICCD '94. Proceedings., IEEE International Conference on](#)
10-12 Oct. 1994 Page(s):145 - 149
Digital Object Identifier 10.1109/ICCD.1994.331875
[AbstractPlus](#) | Full Text: [PDF\(516 KB\)](#) IEEE CNF
[Rights and Permissions](#)

☐ **10. A parallel hashed oct-tree N-body algorithm**

Warren, M.S.; Salmon, J.K.;
[Supercomputing '93. Proceedings](#)
15-19 Nov. 1993 Page(s):12 - 21
Digital Object Identifier 10.1109/SUPERC.1993.1263417
[AbstractPlus](#) | Full Text: [PDF\(742 KB\)](#) IEEE CNF
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2006 IEEE – All Rights Reserved

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	20381	multi-processor or (multiple near processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L13	1566	L10 and L12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L12	3301	L11 and (shared near memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L11	21661	multi-processor or (multiple near processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L10	101715	((("711") or ("710") or ("714"))).CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L7	9	L6 and hash	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L6	21	L2 and (lock near table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L4	8	L3 and hash	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40

EAST Search History

L3	29	L2 and (block near table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L2	3301	L1 and (shared near memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L1	21661	multi-processor or (multiple near processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:40
L14	41	L13 and (hash adj table)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:44

Interference Search.

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L16	1	(seize and address and (hash near (table or list or directory)) and (processor or microprocessor or microcontroller)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:47
L17	25	((seiz\$3 or request\$3 or confiscat\$3) and address\$2 and (hash near (table\$2 or list\$2 or director\$3)) and (processor or microprocessor or microcontroller)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/04 18:48